

RITES LIMITED
(A Govt. of India Enterprise)
RITES Bhawan, Plot No. 1, Sector – 29, Gurgaon – 122001



Recruitment of Engineering professionals on contractual basis

Written Test for following vacancy advertised in August - September 2017 has been scheduled on **26th February 2018** as per following schedule:

VC No.	Post	Time
72/17	Engineer (Electrical)	10:00 AM – 1:00 PM

Candidates are advised to download their Admit Card from the following link:

<https://cdn3.digialm.com:443//EForms/configuredHtml/1839/54231/login.html>

Communication regarding above has been sent to candidates through Email and SMS.

Syllabus of the Written Test has also been mailed to the candidates.

Help Desk:

Candidate(s) having any query regarding Admit Card or exam process may contact on following details:

Helpdesk Number: 022-61087514

Email: helpdesk.rites2017@gmail.com